

FIG. 5

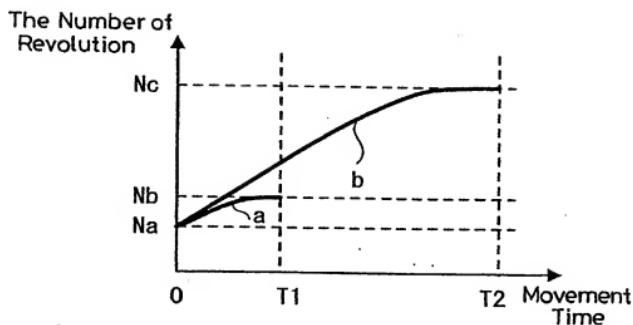


FIG. 6

PRIOR ART

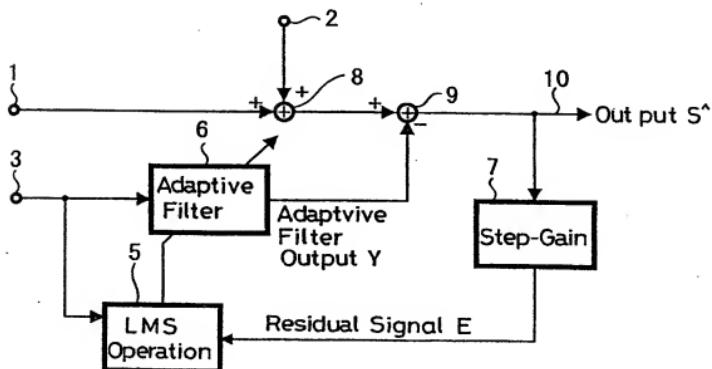
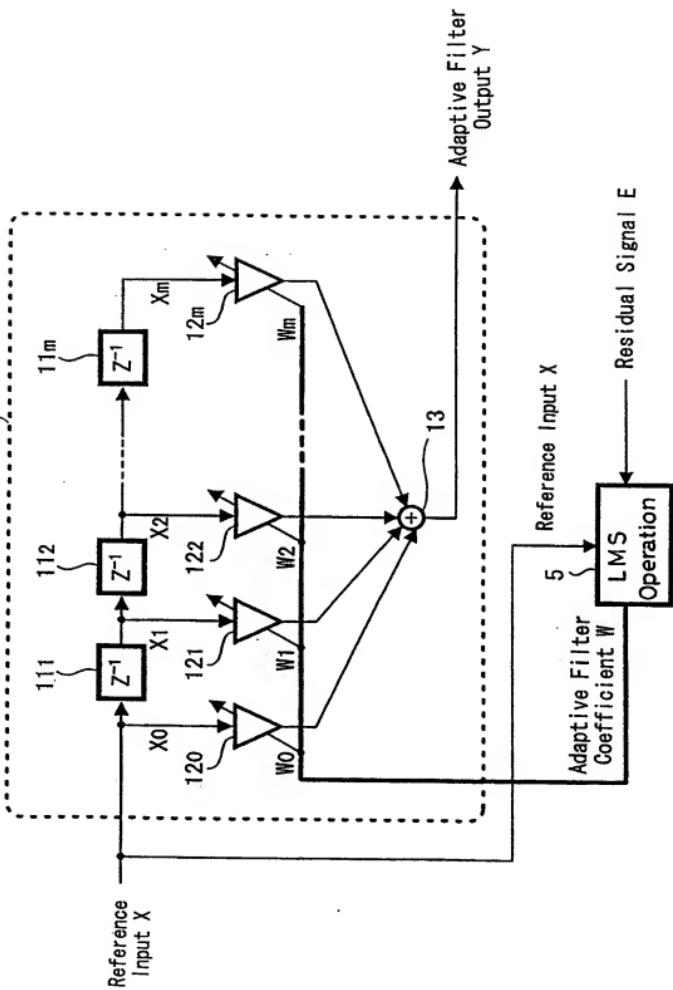


FIG. 7

PRIOR ART



PRIOR ART

FIG. 8

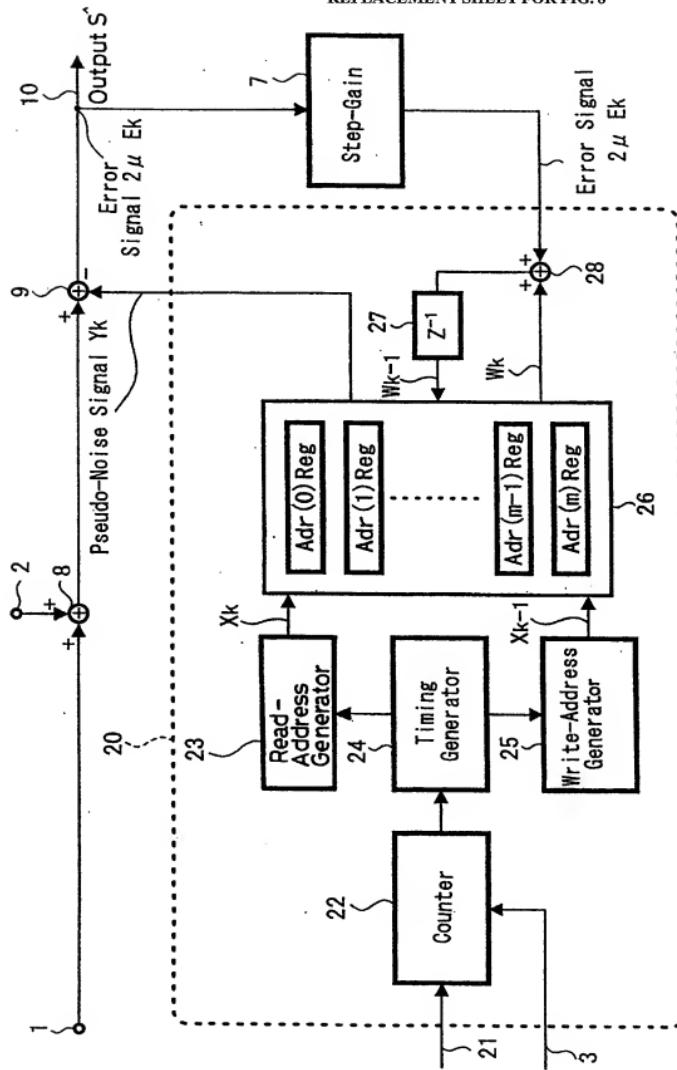
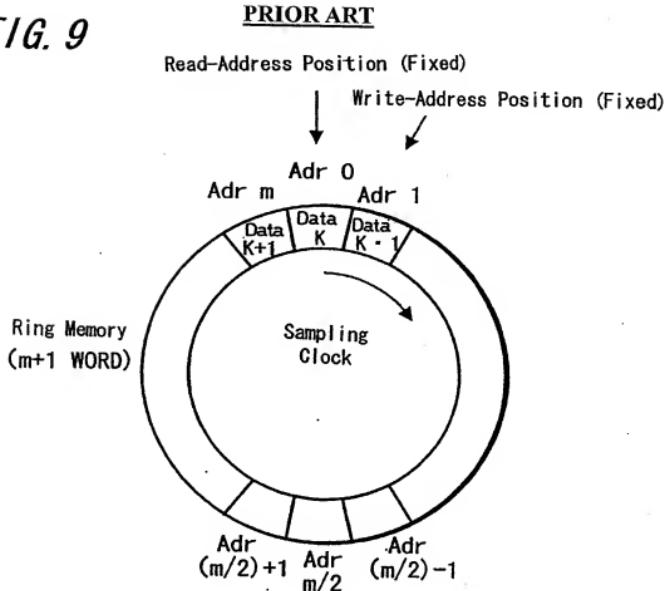


FIG. 9

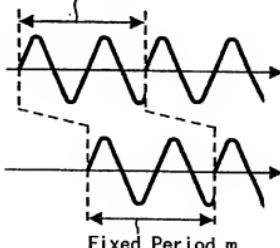


PRIOR ART

FIG. 10A

Write Signal

Fixed Period m



PRIOR ART

FIG. 10B

Read Signal